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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/617,148	07/10/2003	Bruce Gregory Warren	491442011620	1394	
42178 7	590 11/01/2006	EXAMINER			
		URING CORPORATION	MEW, KEVIN D		
•	C/O MORRISON & FOERSTER LLP 555 WEST FIFTH STREET, SUITE 3500			PAPER NUMBER	
LOS ANGELES, CA 90013			2616		

DATE MAILED: 11/01/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

		Applica	tion No.	Applicant(s)			
		10/617,	148	WARREN ET AL.	WARREN ET AL.		
	Office Action Summary	Examine	ər	Art Unit			
		Kevin Me	ew	2616			
Period fo	The MAILING DATE of this communication Reply	on appears on ti	ne cover sheet with t	he correspondence ac	Idress		
WHIC - Exte after - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR INCHEVER IS LONGER, FROM THE MAILING IN INCHEMENT IN I	NG DATE OF T CFR 1.136(a). In no e tion. period will apply and y statute, cause the ap	THIS COMMUNICAT event, however, may a reply to will expire SIX (6) MONTHS oplication to become ABAND	TION.  be timely filed  from the mailing date of this of ONED (35 U.S.C. § 133).	•		
Status							
1)⊠	Responsive to communication(s) filed on	n 02 August 200	16				
· —							
	This action is <b>FINAL</b> . 2b)⊠ This action is non-final.  Since this application is in condition for allowance except for formal matters, prosecution as to the merits is						
closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Dispositi	on of Claims			,			
· · _	Claim(s) 1 and 3-7 is/are pending in the	application	•				
	4a) Of the above claim(s) is/are wi	• •	oneideration				
	Claim(s) is/are allowed.	illidrawii iloili C	onsideration.				
·	Claim(s) <u>1, 3-7</u> is/are rejected.						
· <u> </u>	•						
	Claim(s) is/are objected to.						
ا (٥	Claim(s) are subject to restriction	and/or election	requirement.	•			
Applicati	on Papers			·			
9)[	The specification is objected to by the Ex	aminer.					
10)	The drawing(s) filed on is/are: a)[	accepted or b	) objected to by t	he Examiner.	•		
	Applicant may not request that any objection	to the drawing(s)	be held in abeyance.	See 37 CFR 1.85(a).			
	Replacement drawing sheet(s) including the				FR 1.121(d).		
11)	The oath or declaration is objected to by t	the Examiner. N	lote the attached Of	fice Action or form P	ΓΟ-152.		
Priority ι	inder 35 U.S.C. § 119						
	Acknowledgment is made of a claim for fo		_	9(a)-(d) or (f).			
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-	2. Certified copies of the priority docu		• •				
	3. Copies of the certified copies of the			eived in this National	Stage		
	application from the International E	•	• ••				
* 5	ee the attached detailed Office action for	a list of the cer	tified copies not rece	eived.			
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	e of References Cited (PTO-892)		4) Interview Summ				
	e of Draftsperson's Patent Drawing Review (PTO-94	48)	Paper No(s)/Ma 5) Notice of Inform				
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#### **Detailed** Action

### Response to Amendment

- 1. Applicant's Remarks/Arguments filed on 8/2/2006 regarding claims 1, 3-7 have been considered. Claims 1, 3-7 are currently pending and claim 2 has been canceled by applicant.
- 2. Acknowledgement is made of the amended claim 3 regarding the claim objection on claim 6 set forth in the previous Office action. The corrections are acceptable and the claim objections on claim 6 are now withdrawn.

### Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. Claims 1, 3-4, 6-7 are rejected under 35 U.S.C. 103(a) as being unpatentable over the admitted prior art, Berman (USP 6,118,776) in view of the admitted prior art, Black et al. (USP 6,614,796).

Regarding claim 1, Berman discloses a Fibre Channel Arbitrated Loop interconnect system (Fiber Channel Fabric, Fig. 14) comprising:

a first port (port control module PCM 451, col. 11, lines 30-58, col. 14, lines 4-14 and element 451, Fig. 14) in a plurality of ports (a plurality of ports 451, 455, 474, 475, Fig. 14),

a second port (brouter module or another PCM module, col. 11, lines 30-58 and element 455, Fig. 14) in the plurality of ports (the plurality of ports 451, 455, 474, 475, Fig. 14),

said first and second ports including port logic to monitor Open (OPN) arbitrated loop primitives (port control modules PCM include port logic to monitor arbitrated loop primitives, col. 20, lines, 27-59), and adapted to connect to devices supporting a Fibre Channel Arbitrated Loop protocol (ports supporting FC-2 protocol, col. 11, lines 1-5),

a crossbar switch coupled to the plurality of ports (NxN matrix switch core fabric coupled to the plurality of ports 451, 455, 474, 475, col. 13, lines 63-65 and element 453, Fig. 14),

a route determination apparatus (a router, element 452, Fig. 14) including a routing table (router includes an address match table, col. 13, lines 26-50 and element 531, Fig. 16) comprised of Arbitrated Loop Physical Addresses (ALPAs) (address match table comprises 24-bit addresses, col. 13, lines 26-50), the route determination apparatus separate from the plurality of ports (the plurality of ports 451, 455, 474, 475, Fig. 14) and directly coupled to each of the plurality of ports (the router is directly coupled to each port, Fig. 14) and the crossbar switch (the router is separate from the port controls 451 and 474, Fig. 14) through signaling paths (via signaling paths 459, 460, 461, 462, col. 14, lines 4-14 and Fig. 14), the route determination apparatus for programming the crossbar switch (router programs the switch core to connect routes, col. 12, lines 13-26) to establish direct paths between the first and second ports in the crossbar switch, the direct paths excluding all other ports (NxN matrix

switch core fabric creates direct path from PCM module 451 to Brouter module 455, col. 11, lines 30-58).

Page 4

wherein the crossbar switch creates the direct paths between the first and second ports (NxN matrix switch core fabric creates direct path from PCM module 451 to Brouter module 455 or another PCM module, col. 11, lines 30-58).

wherein priority for each port is independent of the ALPAs (the port with the highest priority will be used which is independent of the addresses, col. 13, lines 18-25).

Berman does not explicitly show the direct paths between the ports are created based on the OPN arbitrated loop primitives.

However, Black discloses a FCAL switching system and method in which the destination address of FCAL OPN primitives are used to establish a direct connection between a source port of a source node and a remote port of a destination node (col. 5, lines 42-47, 66-67, col. 6, lines 1-10).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the fiber channel arbitration method and apparatus of Berman with the teaching of Black in creating a direct path connection between two ports based on the OPN primitives such that FC-AL system and method Berman will create the direct paths between the ports based on the OPN arbitrated loop primitives.

The motivation to do so is to use OPN primitives, instead of the frame of data to find the destination node, to deduce the location of destination node and cut out all subloops and nodes thereon that are not necessary for communication between the source and destination nodes

Application/Control Number: 10/617,148

Art Unit: 2616

thereby decreasing unnecessary delay in completing each loop tenancy and increasing bandwidth.

Regarding claim 3, Berman discloses a system for interconnecting Fibre

Channel Arbitrated Loop devices (a fiber channel private loop device interconnect system, col. 5, lines 22-29) comprising:

a first Arbitrated Loop containing one or more Fibre Channel arbitrated loop devices (a first arbitrated loop containing one or more private loop devices, col. 3, lines 3-13),

a second Arbitrated Loop device (a second arbitrated loop containing one or more private loop devices, col. 3, lines 3-22),

a Fibre Channel Arbitrated Loop interconnect system (a fiber channel private loop device interconnect system, col. 3, lines 3-22), the interconnect system including:

a first port (port 451, Fig. 14) in a plurality of ports (a plurality of ports 451, 455, 474, 475, Fig. 14), the first port containing port logic coupled to the first Arbitrated Loop (port control module PCM 451 contains port logic, col. 11, lines 30-58, col. 14, lines 4-14 and element 451, Fig. 14),

a second port (port 455, Fig. 14) in the plurality of ports (a plurality of ports 451, 455, 474, 475, Fig. 14), the second port containing port logic coupled to the second Arbitrated Loop (brouter module or another PCM module contains port logic, col. 11, lines 30-58 and element 455, Fig. 14),

the first and second ports adapted to connect to devices supporting a Fibre Channel Arbitrated Loop protocol (ports supporting FC-2 protocol, col. 11, lines 1-5);

route determination apparatus (a router, element 452, Fig. 14) separate from the plurality of ports (the router is separate from the port controls plurality of ports 451, 455, 474, 475, Fig. 14) and directly coupled to each of the plurality of ports (the router is directly coupled to each of the ports, Fig. 14) through separate signaling paths for selecting a direct route between the first and second ports (via separate signaling paths 459, 460, 461, 462, col. 14, lines 4-14 and Fig. 14), the direct route excluding all other ports (the path between a PCM module and brouter module or another PCM module is excluding other ports, Fig. 14), and including a routing table (router includes an address match table, col. 13, lines 26-50 and element 531, Fig. 16) comprised of Arbitrated Loop Physical Addresses (ALPAs) and their associated ports (address match table comprises 24-bit addresses, col. 13, lines 26-50)

a crossbar switch directly coupled to the first and second ports and to the route determination apparatus (NxN matrix switch core directly coupled to port control modules and brouter module, col. 13, lines 63-65 and element 453, Fig. 14) through separate signaling paths (via separate signaling paths 476, 457, 458, Fig. 14) for switching frames between ports under control of the route determination apparatus (the router is to connect the frame route between the ports, col. 12, lines 13-26),

wherein Fibre Channel frames are transferred between a device on the first Arbitrated Loop and a device on the second Arbitrated Loop (Fiber Channel frames are routed between device on the fiber channel arbitrated loops, col. 3, lines 3-22), and

wherein priority for each port is independent of the ALPAs (the port with the highest priority will be used which is independent of the addresses, col. 13, lines 18-25).

Berman does not explicitly show selecting the direct path between the first and second ports based on received Fibre Channel Arbitrated Loop primitives from the ports.

However, Black discloses a FCAL switching system and method in which the destination address of FCAL OPN primitives are used to establish a direct connection between a source port of a source node and a remote port of a destination node (col. 5, lines 42-47, 66-67, col. 6, lines 1-10).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the fiber channel arbitration method and apparatus of Berman with the teaching of Black in creating a direct path connection between two ports based on the OPN primitives such that FC-AL system and method Berman will create the direct paths between the ports based on the OPN arbitrated loop primitives.

The motivation to do so is to use OPN primitives, instead of the frame of data to find the destination node, to deduce the location of destination node and cut out all subloops and nodes thereon that are not necessary for communication between the source and destination nodes thereby decreasing unnecessary delay in completing each loop tenancy and increasing bandwidth.

Regarding claim 4, Berman also discloses the interconnect system of claim 3 wherein the Arbitrated Loop primitives that cause the crossbar switch to create paths between ports includes one or more of the following: Arbitrate (ARB), Open (OPN) and Close CLS (OPN primitives includes one of ARBs, OPNs, and Closes, col. 20, lines 41-46).

Page 8

Art Unit: 2616

Regarding claim 6, Berman discloses the interconnect system of claim 3 wherein the first Arbitrated Loop device is on the first port (loop device connected to fiber media is on port control module 451, Fig. 14).

Regarding claim 7, Berman discloses the interconnect system of claim 3 wherein the second Arbitrated Loop device is on the second port (loop device connected to bridged network is on brouter module 455, Fig. 14)

## Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 4. Claim 5 is rejected under 35 U.S.C. 103(a) as being unpatentable over Berman in view of Black, and in further view of Coffey (US Publication 2002/0044561).

Regarding claim 5, Berman and Black disclose all the aspects of the claimed invention set forth in the rejection of claim 3 above, except fail to explicitly show the interconnect system of claim 3 including a R\_RDY counter to count R\_RDY'S sent by the originating Fibre Channel Arbitrated Loop device before the OPN response is received by the originating Fibre Channel Arbitrated Loop Device.

However, Coffey discloses a R\_RDY primitive indicates that an interface buffer is available for receiving frames continuously until something causes the current state to change

(see paragraph 0055, lines 15-20). Coffery further discloses is the OPN primitive is used for opening the connection between the transmitter port and the receiver port (see entire paragraph 0068). Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to combine the fiber channel arbitration method and apparatus of Berman and Black with the teaching of Coffey in using the R\_RDY primitive such that the value of the R\_RDY primitive will be used in the event that the receiver is ready to receive data frames before the originator will receive any opening connection response from the receiver. The motivation to do so is to avoid consuming resources to open connection between two ports unnecessary when the receiver is not yet ready to receive further data frames from the originator.

## Response to Arguments

5. Applicant's arguments filed on 8/2/2006 have been fully considered but they are not moot in view of the new ground(s) of rejection.

In response to applicant's comments on page 1, second paragraph of the Remarks that certain claim amendments were inadvertently not considered in the previous Office action, it is noted that these amendments have been addressed in this new non-final Office action.

Application/Control Number: 10/617,148

Art Unit: 2616

Conclusion

6. Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Kevin Mew whose telephone number is 571-272-3141. The

examiner can normally be reached on 9:00 am - 5:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Seema Rao can be reached on 571-272-3174. The fax phone number for the

organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent

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Page 10

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